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Microcomputer
Repair High-speed
CMOS Logic Data
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Sep. 15, 2003 *

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SMD 85519012A:

Jun. 21, 2016:

Selection guides:

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MIN NOM MAX MIN

NOM MAX VCC

Supply voltage 2 5

6 2 5 6 V VCC = 2

V 1.5 1.5 VIH High-

level input voltage

CC = 4.5 V 3.15 V

VCC = 6 V 4.2 4.2

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VCC = 2 V 0.5 0.5

VIL Low-level input
voltage CC = 4.5 V

1.35 V VCC = 6 V

1.8 1.8 VI Input
voltage 0 VCC 0

VCC V VO Output
voltage 0 VCC 0

VCC V VCC = 2 V
1000 1000

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Title: SN54HC259,
SN74HC259 (Rev.

E) Author: Texas
Instruments,
Incorporated

[SCLS134,E]

Subject: Data

Sheet Keywords:
SCLS134E,SCLS134

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E) - Electronics

Caldas

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TEXAS 75265. D
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Storage Register.
Performs Serial-to-
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Parallel Clear. D
Active-High
Decoder. D Enable
Input ...

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Description for the
SN74HC259. These
8-bit addressable
latches are
designed for
general-purpose
storage
applications in

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digital systems.

Specific uses include working registers, serial-holding registers, and active-high decoders or demultiplexers. They are multifunctional devices capable of storing single-line data in eight addressable

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latches and being a
1-of-8 decoder or
demultiplexer with
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ULN2003 is 2A

stepper motor
driver kit which
features TI's
ULN2003A bipolar
transistor array
and the
CSD1751Q2
NexFET™. This
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contains
everything needed
to drive up to 2
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8 channels, with up to 30V and supports both serial and parallel interfaces (...)

Features. 3.3V, 5V to 30V Selectable split rail supplies for analog and MCU; Supports up to 2.5A ...

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r 1 4 s0 q0 d c r 5

q1 d c r 2 6 s1 q2 d

c r 7 q3 d c r 10 3

q5 s2 d c r 9 q4 d

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c r 7 q3 d c r 10 3

q5 s2 d c r 9 q4 d

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INFORMATION

Orderable Device

Status (1) Package

Type Package

Drawing Pins

Package Qty Eco

Plan (2) Lead

finish/ Ball material

(6) MSL Peak Temp

(3) Op Temp (°C)

Device Marking

(4/5) Samples

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