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A more extensive treatment of logic design is M. Morris Mano "Computer System Architecture", Prentice Hall. We will not need as much as Mano covers and it is not a cheap book so I am not requiring you to get it. I will have it put into the library. My treatment will follow H&P not Mano.

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Class Notes for Computer Architecture

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simplification using Karnaugh map, Don't Care Conditions, flip-flops, characteristic tables.

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Note Computer System Architecture CSA By Rajat Kumar S ...

Computer Architecture - Class notes. Table of Contents. Chapter 1 - Fundamentals of Computer Design. Chapter 2 - Basic Organization of a Computer. Chapter 3 - Instruction Set Design. Chapter 4 - Addressing Modes. Chapter 5 - CPU

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Implementation. Chapter 6 - Interrupts.

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Single Cycle Datapath, Designing Single Cycle Control, Designing a Multiple Cycle  
Processor , Designing a Multiple Cycle Controller, Designing a Pipeline Processor,  
Cache Memory, Memory Systems , Virtual Memory.

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SHRI VISHNU ENGINEERING COLLEGE FOR WOMEN::BHIMAVARAM ...

Course Contents □ This course shall cover the following main topics – microcomputer – microprocessor register – ALU design – control unit design – instruction cycle – memory types – Read/Write cycles – memory mapping – address decoding – address decoder design – Interrupts – Polling – I/O devices interfacing – DMA – bus arbitration – introduction to CISC/RISC architecture EE-222 Computer Architecture Muhammad Bilal Saif 4

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Lecture 1 - SlideShare

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Lecture 5 : Instruction Set ...

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Overview This course is a study of the evolution of computer architecture and the factors influencing the design of hardware and software elements of computer systems.

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