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Reduced Set Instruction Set
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Patterson and Hennessy brilliantly address the issues in the ever changing computer hardware architecture. Professor Jae Oh, Syracuse. Authors: David Patterson & John Hennessy. Availability: Now. Edition: RISC-V 5th. Pages: 676. Price on Amazon: \$65. ISBN-13: 978-0128122754.

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4 The Processor §4.1 Introduction

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RISC chips require fewer transistors which make them cheaper to design and produce. In RISC, the instruction set contains simple and basic instructions from which more complex instruction can be produced. Most instructions complete in one cycle, which allows the processor to handle many instructions at same time.

RISC and CISC Processors |
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A reduced instruction set computer (RISC /pronounce as [ˈrɪsk]/) is a

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computer which only use simple instructions that can be divide into multiple instructions which perform low-level operation within single clock cycle, as its name suggest

▣REDUCED INSTRUCTION SET▣

Understand RISC & CISC architecture with example

Difference between RISC and CISC architecture

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