

Arm Architecture Reference Manual Supplement

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introduced at the same time. Thumb-2 is a superset of the ARMv6 Thumb ISA described in the ARM Architecture Reference Manual (ARM DDI 0100). Thumb-2 extends the Thumb architecture by adding the following: † A substantial number of new 32-bit Thumb instructions. These cover most of the functionality of the ARM instruction set.

ARM Architecture Reference Manual Thumb-2 Supplement
Arm® Architecture Reference Manual Supplement - The Scalable Vector Extension (SVE), for Armv8-A; Arm® Architecture Reference Manual Supplement Memory System Resource Partitioning and Monitoring (MPAM), for Armv8-A ; Arm® Reliability, Availability, and Serviceability (RAS) Specification - Armv8, for the Armv8-A architecture profile; Arm® Architecture Reference Manual Armv7-A and Armv7-R edition

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